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Maca et al.

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(54) **ADAPTIVE CANCELLATION FOR WIRELESS REPEATERS**(75) Inventors: **Gregory A. Maca; Mano D. Judd,**  
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(22) Filed: Jan. 14, 2000

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(51) Int. Cl. 7 .....

**H04B 7/15**(52) U.S. Cl. .... 455/11.1; 455/24; 455/63;  
455/67.1; 455/67.3; 455/562(58) **Field of Search** .... 455/7, 11.1, 24,  
455/278.1, 63, 67.1, 67.3, 280, 296, 90,  
422, 561, 562, 571; 343/702, 824(56) **References Cited****U.S. PATENT DOCUMENTS**

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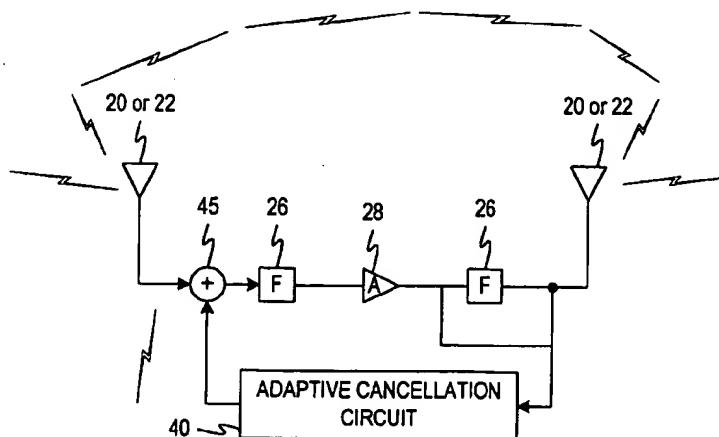
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*Primary Examiner*—Quochien B. Vuong(74) *Attorney, Agent, or Firm*—Wood, Herron & Evans, L.L.P.(57) **ABSTRACT**

A repeater system has at least one donor antenna element for communicating with a base station and at least one link antenna element for communicating with a remote station. A radio frequency uplink path and a radio frequency downlink path are coupled between the donor antenna and the null antenna. An adaptive cancellation circuit in each of the radio frequency uplink path and the radio frequency downlink path generates a cancellation signal, which when added to a radio frequency signal in either of the radio frequency uplink and radio frequency downlink paths, substantially cancels any feedback signal present in the radio frequency signal.

**31 Claims, 6 Drawing Sheets**

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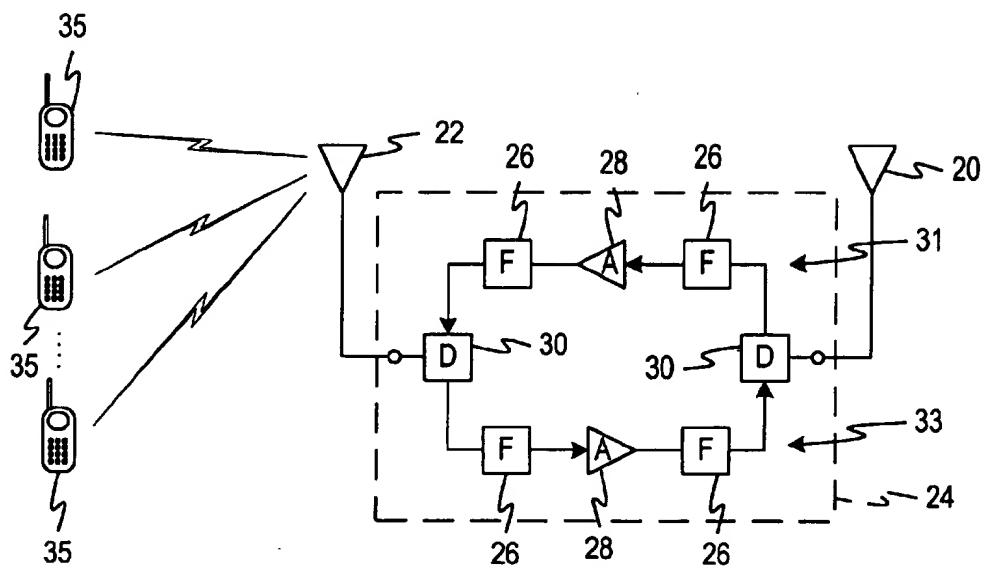


FIG. 1

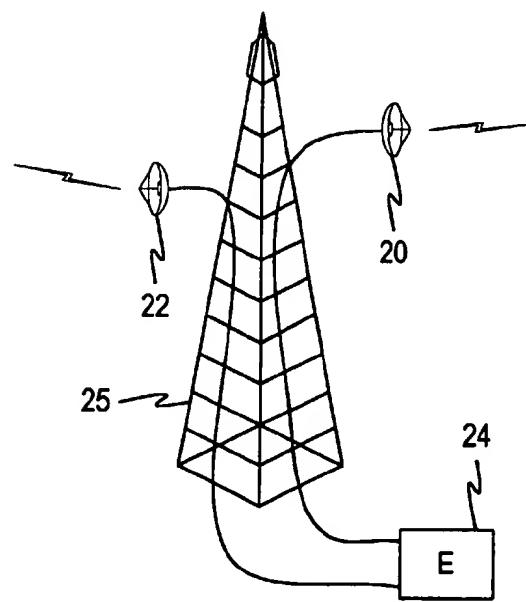


FIG. 2

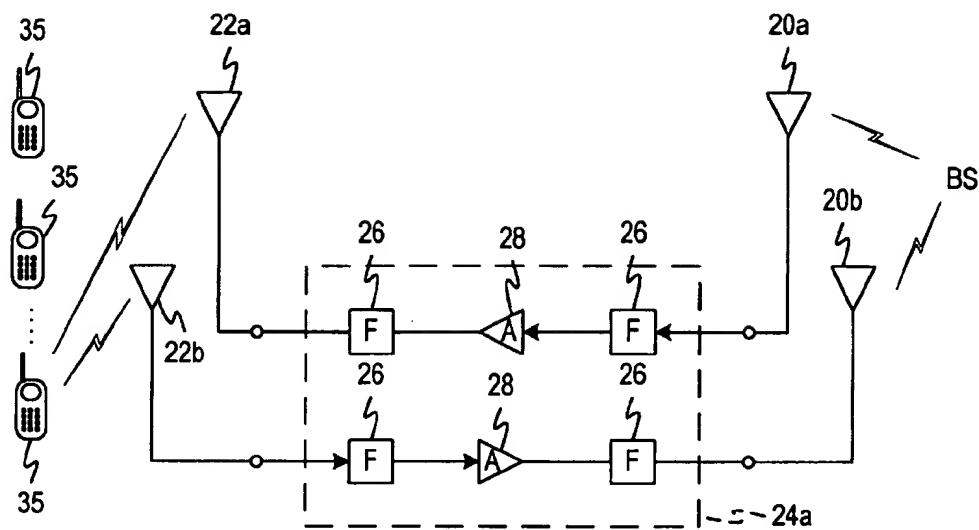


FIG. 3

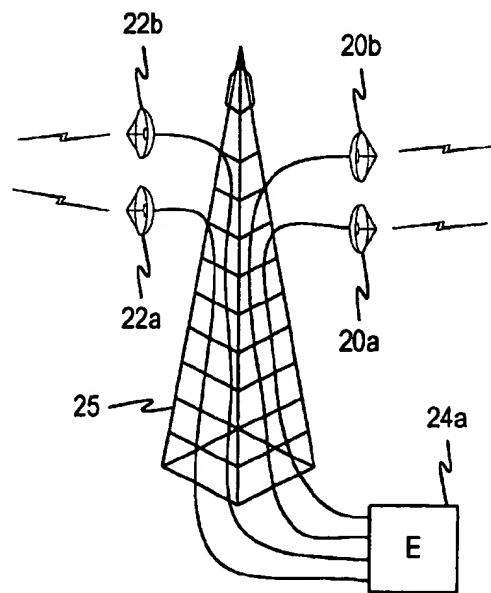


FIG. 4

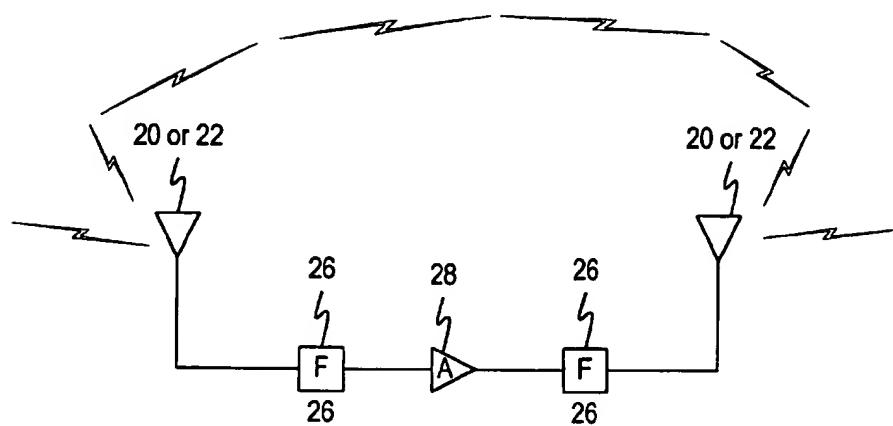


FIG. 5

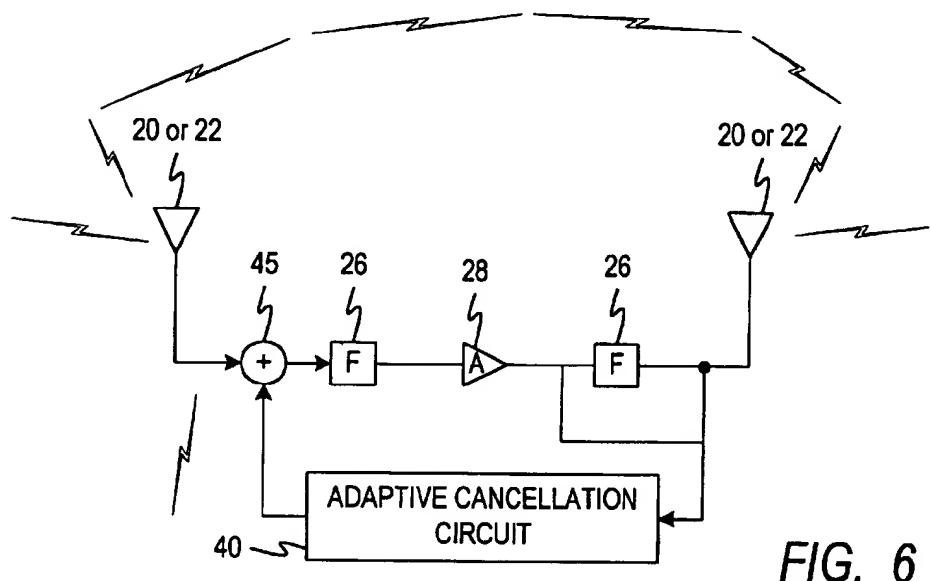


FIG. 6

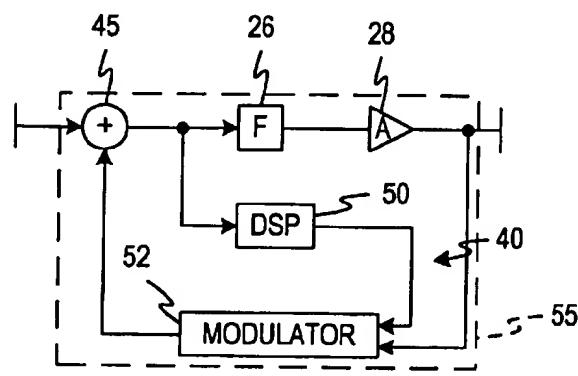
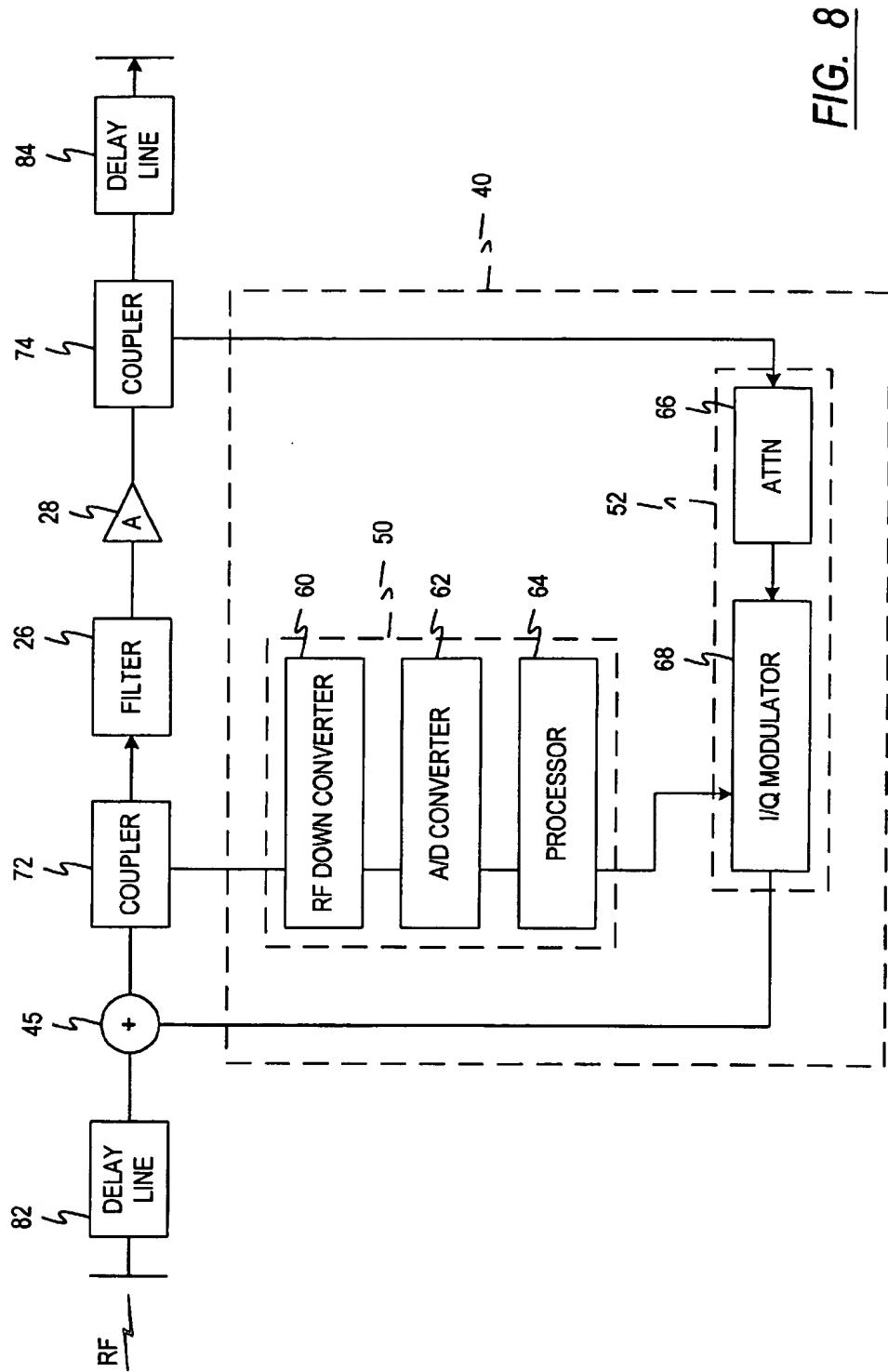


FIG. 7



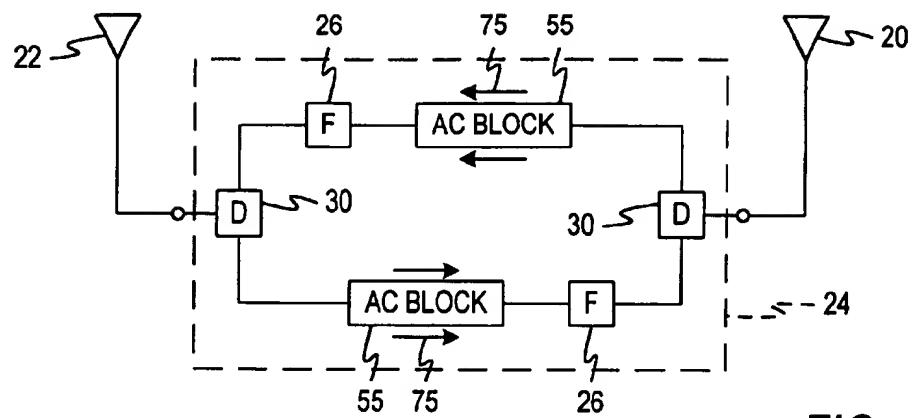


FIG. 9

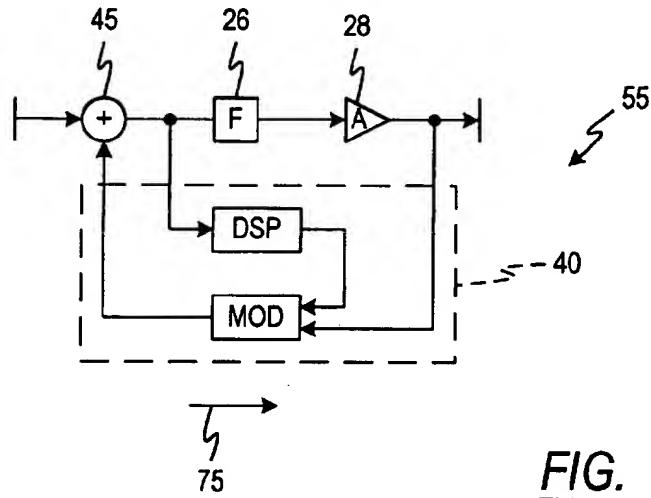


FIG. 10a

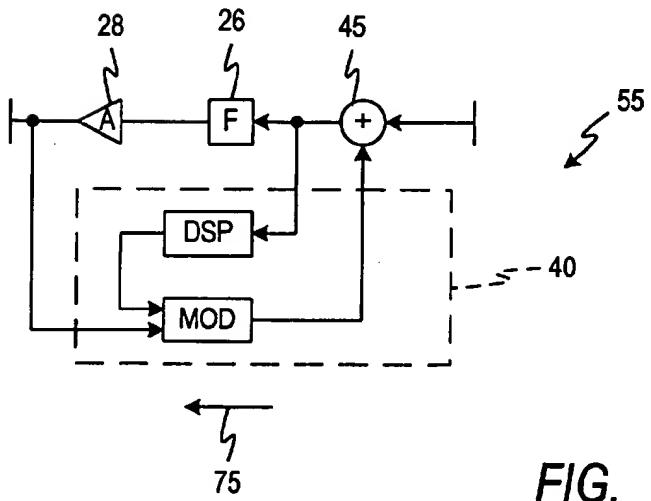
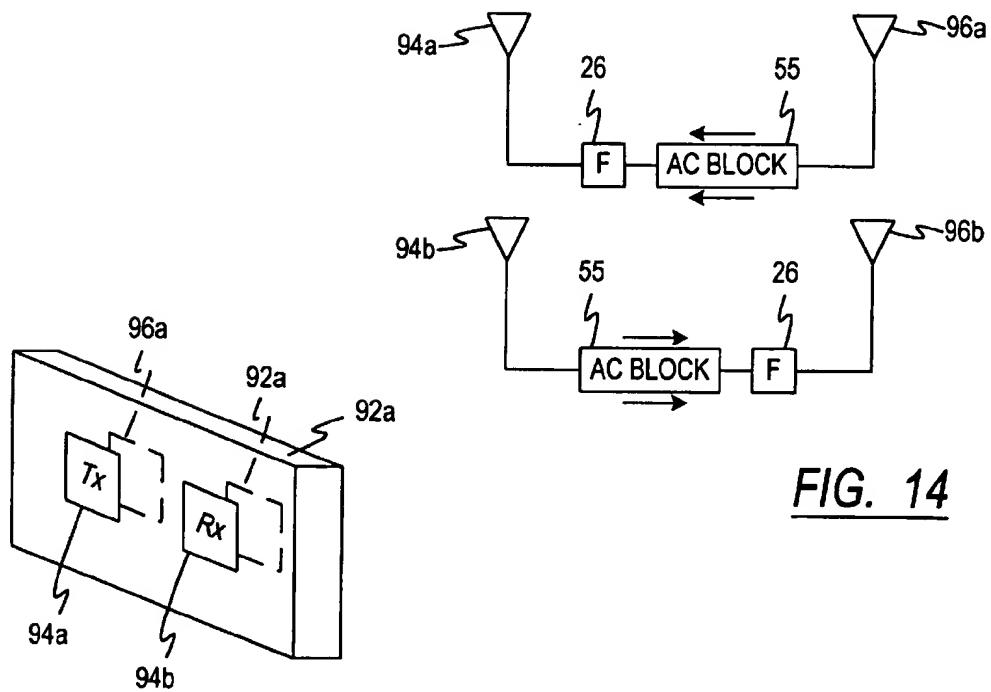
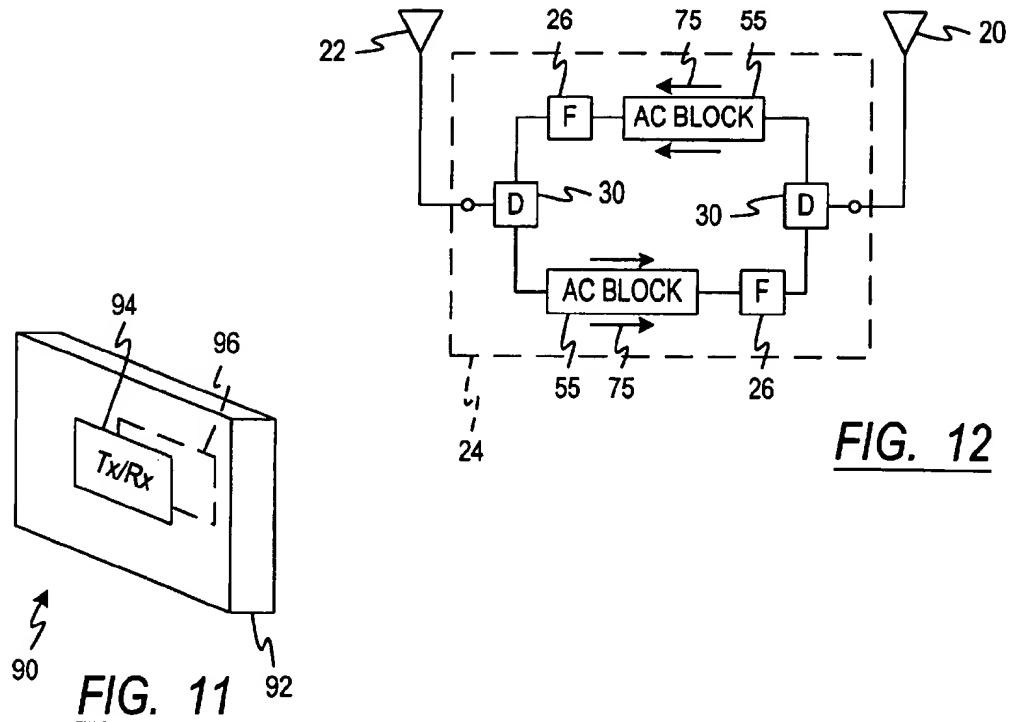


FIG. 10b



## ADAPTIVE CANCELLATION FOR WIRELESS REPEATERS

### CROSS-REFERENCE TO RELATED APPLICATION

This application is a continuation-in-part of prior application Ser. No. 09/357,032, filed Jul. 20, 1999 entitled "Side-to-Side Repeater".

### FIELD OF THE INVENTION

This invention is directed generally to repeater systems for wireless communications and more particular to a method and apparatus for improving the isolation between donor and null antennas in a repeater system.

### BACKGROUND OF THE INVENTION

In existing wireless technologies, such as Cellular, PCS (personal communication services), MMDS (multi-user multipath distribution system), WLL (wireless local loop), or the like, repeaters are used to extend coverage of cell sites (base stations) to areas that have low signal reception power, and therefore low signal to noise ratios. One of the principle technology drivers is that of system gain. That is, the higher the gain, the greater the range (distance) and coverage area for the system.

A limiting characteristic for repeaters is that of the feed-back loop, or conversely, the isolation between the two opposing antennas (or sensors). That is, the total front to back (F/B) ratio for the system, or isolation, must be higher than the desired gain. Usually, repeaters employ at least one "link" or "donor" antenna which communicates with the base station and at least one broadcast, coverage, or "null" antenna which communicates with the remote stations or subscribers. Generally speaking, the isolation between link and null antennas is equal to the total gain plus some margin, typically around 10 to 15 dB. Therefore, the system gain will in general be less than the isolation minus the margin. For example, if the isolation between antennas is around 60 dB, then the maximum system gain allowed will be about 45 dB. For PCS frequencies, these figures result in a repeater range of less than 100 feet.

In a scattering environment, which is common in PCS, every 6 dB of additional system gain will double the coverage distance. Thus, obtaining an additional 24 dB of isolation between the two antennas, will allow the range to double 4 times, to 1600 feet. For conventional repeater systems, where the two antennas and repeater electronics are in three separate enclosures, and locations, the link (donor) antenna (to the base station) and broadcast (null) antenna (to the desired coverage area), are separated in space by (usually) more than 10 feet. This distance adds over 50 dB to the isolation between antennas, generating a total isolation value of well over 100 dB. Therefore, with a 15 dB margin, this type of system can utilize a total gain of up to 85 dB or more, which results in fairly large range and coverage.

For the side-to-side repeater technologies (as described in the above-referenced prior application), where the opposing antennas are in the same enclosure, and separated in space often by less than a few inches, the F/B ratio (or isolation) is typically limited to a value below 80 dB or so. This therefore allows a total system gain of no more than 65 dB, which limits the system range to a few hundred feet or less.

### SUMMARY OF THE INVENTION

The invention provides a method and apparatus for improving isolation between donor and null antennas in a repeater system.

The described embodiment of the invention provides an adaptive cancellation approach which removes a significant portion (between 10 dB and 40 dB), of the feedback signal power, therefore increasing the total system isolation by the same amount (10 to 40 dB). This additional isolation can be used to achieve greater system gain, and therefore significantly extend the range of the system. This is especially useful in the side-side repeater technology approach mentioned above and described in the above-referenced application. The cancellation scheme uses digitally processed information to generate a signal, which, when added to the original input signal, cancels the feedback signal.

### BRIEF DESCRIPTION OF THE DRAWINGS

15 In the drawings:

FIG. 1 is a block diagram of one form of a conventional repeater system;

20 FIG. 2 is a diagram of a typical repeater tower, with the conventional repeater system of FIG. 1;

25 FIG. 3 is a block diagram of another form of a conventional system (similar to FIG. 1), with four distinct input/output ports for separate uplink and downlink antennas;

30 FIG. 4 shows a tower-top arrangement, using the four-port system of FIG. 3;

35 FIG. 5 is a block diagram of one signal path through a repeater system;

40 FIG. 6 is a block diagram of one signal path through a repeater system, as in FIG. 5, adding an adaptive cancellation circuit;

45 FIG. 7 is a block diagram (high level) of a (digitally) adaptive cancellation circuit to in accordance with one embodiment of the invention;

50 FIG. 8 is a block diagram (high level) of the (digitally) adaptive cancellation circuit of FIG. 7 which shows the technique in further detail;

55 FIG. 9 is a block diagram of a repeater system, similar to that of FIG. 1, using the adaptive cancellation (AC) circuit of FIGS. 7 and 8;

60 FIGS. 10a and 10b show the directional characteristics of the AC blocks, for the downlink path (FIG. 10a) and the uplink path (FIG. 10b);

65 FIGS. 11 and 13 show two examples of side-to-side repeaters; and

70 FIGS. 12 and 14 show block diagrams of the AC approach of the invention, applied to the side-to-side repeater systems of FIGS. 11 and 13, respectively.

### DETAILED DESCRIPTION OF THE ILLUSTRATED EMBODIMENT

FIG. 1 shows a block diagram of a conventional repeater system, showing three distinct elements: the link (or "donor") antenna, the coverage (or "null") antenna 22, and the repeater electronics 24. The typical repeater electronic system 24 incorporates bandpass filters 26, for the uplink and downlink bands, as well as power amplifiers 28 and frequency diplexers 30. The filters 26 and amplifiers 28 are arranged as a pair of signal transmission circuits 31 (downlink) and 33 (uplink). In each circuit 31, 33, a first filter 26 attenuates the bandwidth of incoming communications signals, an amplifier 28 amplifies the signals from the first filter, and a second filter 26 located at the output of the amplifier protects the amplifier 28 from signal power in the other branches of the circuits 31, 33. The illustrated system in FIG. 1 is based on a "straight through" design, in which

the signals go in a single direction through each branch or path (i.e., uplink and downlink) of the system.

FIG. 2 shows a diagram of a typical repeater tower 25, with the system of FIG. 1, including the link (or "donor") antenna 20, the coverage (or "null") antenna 22, and the repeater electronics 24. This system could also be installed on the top of a building, or within a building (for in-building coverage).

FIG. 3 shows a similar system to that of FIG. 1, but with four distinct input/output ports. In this system, the signals from each path are not combined by frequency diplexers. Additionally, this system requires four, rather than two, antennas. Accordingly, FIG. 3 shows two donor antennas 20a and 20b, designated as the downlink and uplink to the base station (BS). Similarly, on the broadcast or coverage side, two null antennas 22a, 22b are shown, one functioning as the uplink and one as the downlink antenna relative to the remote units or subscriber equipment 35. Since separate antennas are used respectively for the uplink and downlink between the base station and the remote units, separate paths through the electronics 24a, each including their own filters 26 and amplifier 28 are provided, and diplexers such as the diplexers 30 of FIG. 1 are not utilized.

FIG. 4 shows a tower arrangement, using the four-port system of FIG. 3. In FIG. 4, the respective downlink and uplink antennas 20a, 22a and 20b, 22b between the base station and the remotes are illustrated on the tower 25, with the electronics 24a of FIG. 3 being located at the base of the tower 25.

FIG. 5 shows a block diagram of one path through a repeater system. The input signal,  $S(t)$ , either from the base station (for the downlink path), or from the remote (for the uplink path), is received via an antenna, bandpass filtered, amplified (with active gain=G), filtered again, and finally transmitted by an antenna. Some of the transmitted signal energy couples back (through space, or through the electronics) into the receive antenna. This is denoted in FIG. 5 as the feedback signal,  $f(t)$ , which is simply a delayed version (attenuated) of the original signal,  $S(t)$ . Therefore, the composite signal,  $S(t)+f(t)$ , is fed into the amplifier, with output  $G(S(t)+f(t))$ . Assume for example, that the antennas have 0 dB gain, then the new feedback signal is  $G f(t)$ . The propagation of this signal, back to the input antenna, will incur attenuation,  $H$ . Therefore, the amplified, attenuated signal at the input antenna will be  $H G f(t)$ . If this signal is comparable in power to the original signal  $S(t)$ , then the amplifier 28 will go unstable, and oscillate (ring). This oscillation will cause severe distortion in the desired signal.

FIG. 6 shows the same circuit as FIG. 5; however, adding an adaptive cancellation circuit 40. The goal of this circuit 40 is to create a inverse  $f(t)$  signal- $f(t)$  (a 180 degree shifted  $f(t)$  signal), and sum it with the input signal; including the feedback signal,  $f(t)$  at a summing junction 45, and thereby remove the feedback signal  $f(t)$ .

FIG. 7 shows a general block diagram (high level) of one form of the adaptive cancellation circuit 40. In this approach, the input (RF) signal is summed at the junction 45 with a modulated signal constructed via a digitally adaptive process, to destructively interfere with the feedback signal embedded in the input composite signal. After the summation, the composite signal,  $S(t)+f(t)$ , is digitally sampled and digitally processed via a digital signal processor (DSP) 50, which computes an intermediate signal for a modulator 52. The modulator 52 takes the intermediate signal, and a sample of the amplified (output) signal, and creates a near copy of the correct inverted  $f(t)$  signal- $f(t)$ .

This process will work with many, if not most, of the digitally adaptive algorithms for feedback control. Additionally, this methodology does not require an injected signal (training or pilot tone, or wideband noise), for the adaptive process. The entire circuit shown in FIG. 7 may be designated as an "AC block" 55.

FIG. 8 shows the circuit 40 in further detail. The DSP 50 is a combination of an RF downconverter 62 to shift the signal to an intermediate frequency that allows digital sampling, an analog-to-digital (A/D) converter 64 which digitizes the analog signal, and a processor 66 which performs the required operations to compute the intermediate signal. The modulator 52 is a combination of a controllable attenuator 66, and an I/Q modulator 68. Additional details shown in FIG. 8 include respective couplers 72 and 74 which couple the signals from the signal path to and from the adaptive cancellation circuit 40, a first coupler 72 being interposed between the summation junction 45 and the filter 26 and the second coupler 74 being at the output of the power amplifier 28. In addition to the couplers 72 and 74 respective delay lines 82 and 84 may be employed at either end of the RF path, one just prior to the summing junction 45 and one subsequent to the coupler 74.

FIG. 9 shows a block diagram of a repeater system, similar to that of FIG. 1, however using adaptive cancellation (AC) circuit blocks 55, details of which are shown in FIGS. 10a and 10b, as well as in FIGS. 7 and 8. The "direction" of the circuit (AC block) 55 in each RF path has been taken into account. In this system, each (uplink, downlink) path has a separate AC circuit block 55.

FIGS. 10a and 10b show the directional characteristics of the AC circuit blocks 55, whether for the downlink or uplink path. The blocks are "mirror images" of one another, differing by the direction of the desired signal, with the arrows 75 denoting the directionality of each circuit 55, in FIGS. 9, 10a and 10a.

FIGS. 12 and 14 show block diagrams of the adaptive cancellation (AC) approach, applied to side-to-side repeater systems, such as the ones shown and described in the above-referenced prior application. FIG. 11 shows a side-to-side repeater having a body or housing 92 having opposed flat surfaces. To each of these opposed flat surfaces, there is mounted a single patch antenna element 94, 96, respectively comprising the null and donor antennas. An equivalent circuit diagram is shown in FIG. 12. It will be understood that the circuit components of FIG. 12, including the adaptive cancellation (AC) circuits, may be carried in the body or housing 92.

Similarly, FIG. 13 shows a side-to-side repeater structure 90a having a similar body or housing 92a which mounts separate uplink and downlink transmit (Tx) and receive (Rx) patch antenna elements for each of the null and donor antennas. The respective Tx and Rx null antennas are designated by reference numerals 94a and 94b, while the respective Tx and Rx donor antenna elements are designated by reference numerals 96a and 96b. The circuits shown in FIG. 14 may be mounted to (in) the body (housing) 92a.

As indicated above, the electronics, i.e., the adaptive cancellation circuits, may be carried on/in the body/housing 92, 92a of the antenna element in the side-to-side repeater structures of FIGS. 11 and 13, permitting a tower-top modular repeater installation, in addition to the other advantages, and/or use in other applications as more fully discussed and described in the above-referenced prior application.

As indicated above, FIGS. 11 and 12 show the case for a single antenna (element) on each side, including frequency

diplexers to separate each path (or frequency band). FIG. 13 and 14 show the approach when using separate Tx and Rx antennas, and therefore separate circuits, for each (uplink, downlink) path.

The above-described approach may be used in a number of applications, including: Cellular Coverage (null fill, in-building systems), PCS, MMDS, WLL and LMDS.

While particular embodiments and applications of the present invention have been illustrated and described, it is to be understood that the invention is not limited to the precise construction and compositions disclosed herein and that various modifications, changes, and variations may be apparent from the foregoing descriptions without departing from the spirit and scope of the invention as defined in the appended claims.

What is claimed is:

1. A repeater system comprising:

a housing having a pair of substantially 180° oppositely facing surfaces;

at least one donor antenna element for communicating with a base station and at least one null antenna element for communicating with a remote station, wherein said donor and null antenna elements are respectively mounted on said oppositely facing surfaces for radiating energy in opposite directions;

a radio frequency uplink path and a radio frequency downlink path coupled between said donor antenna and said null antenna; and

an adaptive cancellation circuit in at least one of said radio frequency uplink path and said radio frequency downlink path and mounted to said housing, which generates a cancellation signal, which when added to a radio frequency signal in either of said radio frequency uplink and radio frequency downlink paths, destructively interferes with any feedback signal present in said radio frequency signal.

2. The system of claim 1 wherein said adaptive cancellation circuit comprises a digital signal processor circuit which receives an incoming radio frequency signal from one of said radio frequency uplink and downlink paths, said radio frequency signal having a feedback signal component, and which digitally samples and processes said incoming radio frequency signal to generate an intermediate frequency signal, and a modulator circuit which receives said intermediate frequency signal and a sample of a radio frequency output signal from said one of said radio frequency uplink and downlink paths and generates said cancellation signal.

3. The system of claim 2 wherein said digital signal processor comprises a radio frequency downconverter which converts said incoming radio frequency signal to a lower frequency signal for digital sampling, an analog-to-digital converter coupled to the radio frequency downconverter, which analog-to-digital converter digitizes said lower frequency signal, and a processor coupled to the analog-to-digital converter which computes a desired intermediate frequency signal for the modulator.

4. The system of claim 2 wherein said adaptive cancellation circuit further includes a summing junction which receives and sums said intermediate frequency signal and said incoming radio frequency signal.

5. The system of claim 4 wherein said digital signal processor circuit receives an output of said summing junction.

6. The system of claim 5 wherein said adaptive cancellation circuit further includes a filter coupled to receive the output of said summing junction and a power amplifier coupled to receive the output of said filter.

7. The system of claim 3 wherein said modulator circuit comprises a controllable attenuator which receives and attenuates the radio frequency output signal and an I/Q modulator coupled to said attenuator and to said processor.

8. The system of claim 1 wherein a single antenna element is mounted to each of said oppositely facing surfaces of said housing and wherein said electronic circuit includes a frequency diplexer operatively coupled with each of said antennas and a pair of signal transmission circuits coupled between said frequency diplexers.

9. The system of claim 8 wherein each of said signal transmission circuits transmits a signal in one direction between said two antenna elements, each of said signal transmission circuits comprising a first filter to attenuate the bandwidth of incoming communications signals, an amplifier to amplify communications signals received from said first filter and a second filter located at an output of said amplifier to protect the amplifier from signal power in the other signal transmission circuit.

10. The system of claim 1 wherein two antenna elements are mounted to each of side of said housing, one for transmitting communications signals and one for receiving communications signals.

11. The system of claim 10 wherein said electronic circuit comprises a pair of signal transmission circuits, each forming a signal path between a transmitting antenna on one surface of said housing and a receiving antenna on the opposite surface of said housing, and wherein each of said signal transmission circuits comprises a first filter to attenuate the bandwidth of incoming communications signals, an amplifier which amplifies communications signals received from the first filter, and a second filter located at an output of said amplifier to protect the amplifier from signal power in the other of said signal transmission circuits.

12. The system of claim 1 wherein each of said antenna elements comprises a microstrip patch.

13. The system of claim 1 wherein said adaptive cancellation circuit is housed within said housing.

14. The system of claim 13 wherein said adaptive cancellation circuit comprises a digital signal processor circuit which receives an incoming radio frequency signal from one of said radio frequency uplink and downlink paths, said radio frequency signal having a feedback signal component, and which digitally samples and processes said incoming radio frequency signal to generate an intermediate frequency signal, and a modulator circuit which receives said intermediate frequency signal and a sample of a radio frequency output signal from said one of said radio frequency uplink and downlink paths and generates said cancellation signal.

15. The system of claim 11 wherein said adaptive cancellation circuit further includes a summing junction which receives and sums said cancellation signal and said incoming radio frequency signal.

16. The system of claim 15 wherein said digital signal processor circuit receives an output of said summing junction.

17. The system of claim 16 wherein said adaptive cancellation circuit further includes a filter coupled to receive the output of said summing junction and a power amplifier coupled to receive the output of said filter.

18. A method of operating a repeater system having a housing with a pair of substantially 180° oppositely facing surfaces, at least one donor antenna element for communicating with a base station and at least one null antenna element for communicating with a remote station, and a radio frequency uplink path and a radio frequency downlink path coupled between said donor antenna and said null

antenna, wherein said donor and null antenna elements are respectively mounted on said oppositely facing surfaces for radiating energy in opposite directions, said method comprising:

generating, within an electrical circuit mounted to said housing, a cancellation signal in at least one of said radio frequency uplink path and said radio frequency downlink path, which cancellation signal, when added to a radio frequency signal in either of said radio frequency uplink and radio frequency downlink paths, destructively interferes with any feedback signal present in said radio frequency signal.

19. The method of claim 18 wherein said generating comprises receiving an incoming radio frequency signal from one of said radio frequency uplink and downlink paths, said radio frequency signal having a feedback signal component, digitally sampling and processing said incoming radio frequency signal to generate an intermediate frequency signal, and using said intermediate frequency signal and a sample of a radio frequency output signal from said one of said radio frequency uplink and downlink paths to generate said cancellation signal.

20. The method of claim 19 wherein said processing comprises downconverting said incoming radio frequency signal to a lower frequency signal for digital sampling, digitizing said lower frequency signal, and digitally computing a desired intermediate frequency signal using said digitized lower frequency signal.

21. The system of claim 19 further including a summing said intermediate frequency signal and said incoming radio frequency signal.

22. The method of claim 21 wherein said digitally processing follows said summing.

23. The method of claim 22 further including filtering and power amplifying following said summing.

24. The method of claim 20 wherein said generating further comprises controllably attenuating the radio frequency output signal and I/Q modulating following said attenuating.

25. A repeater system comprising:

a housing having a pair of substantially 180° oppositely facing surfaces;

at least one donor antenna element for communicating with a base station and at least one null antenna element for communicating with a remote station, wherein said

5 donor and null antenna elements are respectively mounted on said oppositely facing surfaces for radiating energy in opposite directions; and  
an adaptive cancellation circuit mounted to said housing, and which generates a cancellation signal, which when added to a radio frequency signal from either of said donor antenna and said null antenna, destructively interferes with any feedback signal present in said radio frequency signal.

10 26. The repeater system of claim 25 wherein said adaptive cancellation circuit comprises a digital signal processor circuit which receives an incoming radio frequency signal from one of said donor and null antennas, said radio frequency signal having a feedback signal component, and which digitally samples and processes said incoming radio frequency signal to generate an intermediate frequency signal, and a modulator circuit which receives said intermediate frequency signal and sample of a radio frequency output signal from said one of said donor and null antennas and generated said cancellation signal.

15 27. The system of claim 26 wherein said digital signal processor comprises a radio frequency downconverter which converts said incoming radio frequency signal to a lower frequency signal for digital sampling, an analog-to-digital converter coupled to the radio frequency downconverter, which analog-to-digital converter digitizes said lower frequency signal, and a processor coupled to the analog-to-digital converter which computes a desired intermediate frequency signal for the modulator.

20 28. The system of claim 26 wherein said adaptive cancellation circuit further includes a summing junction which receives and sums said intermediate frequency signal and said incoming radio frequency signal.

29. The system of claim 28 wherein said digital signal processor circuit receives an output of said summing junction.

30 35 30. The system of claim 29 wherein said adaptive cancellation circuit further includes a filter coupled to receive the output of said summing junction and a power amplifier coupled to receive the output of said filter.

40 31. The system of claim 27 wherein said modulator circuit comprises a controllable attenuator which receives and attenuates the radio frequency output signal and an I/Q modulator coupled to said attenuator and to said processor.

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